FIG.1

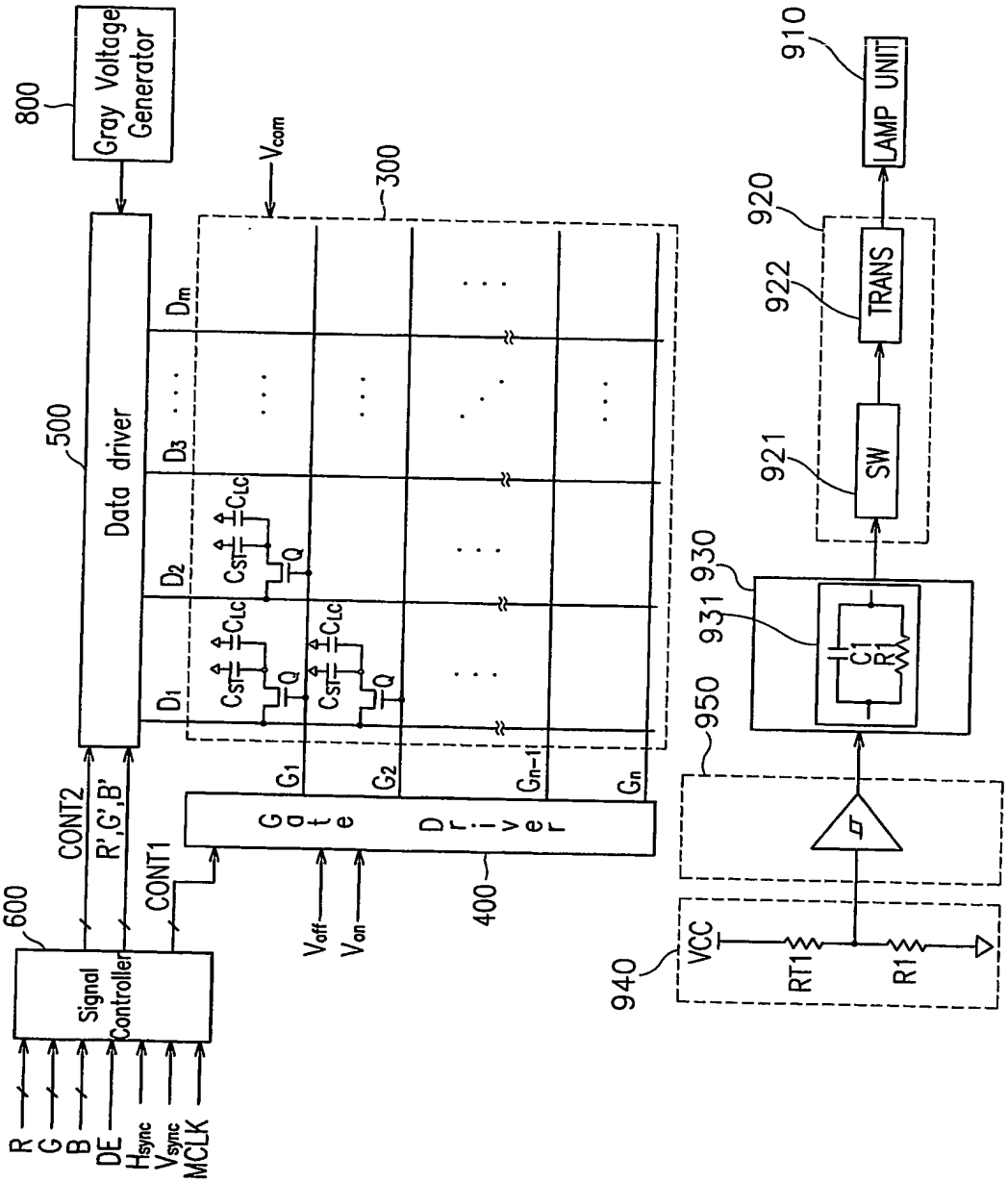

800
Gray Voltage Generator
Vcom
300
500
Data driver
600
Signal Controller
CONT1
CONT2
R',G',B'
R
G
B
DE
Hsync
Vsync
MCLK
Gate Driver
400
Voff
Von
G1
G2
Gn-1
Gn
D1
D2
D3
Dm
CST
CLC
Q
910
LAMP UNIT
920
922
TRANS
921
SW
930
931
C1
R1
950
940
VCC
RT1
R1

FIG.2

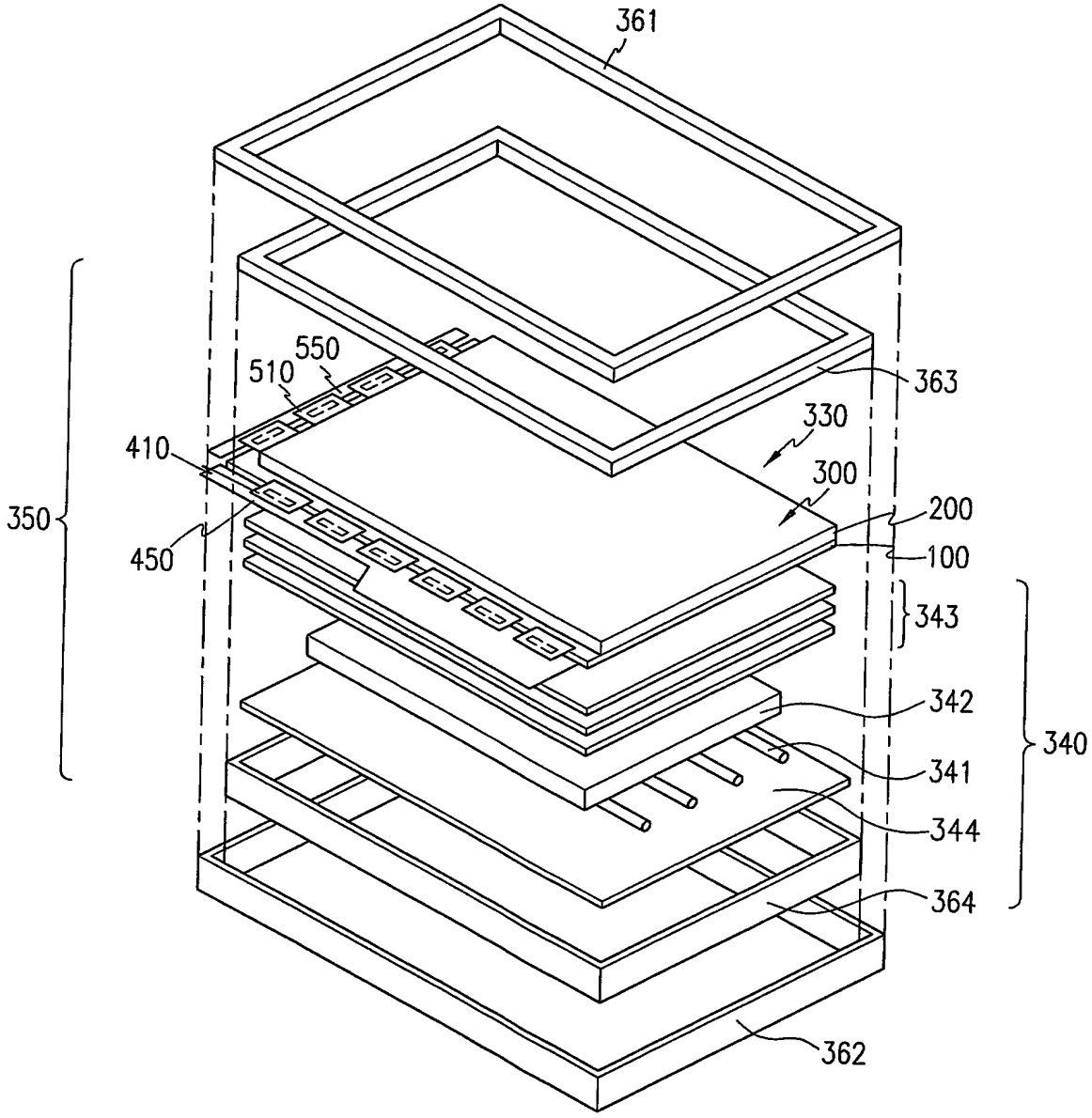

361
550
510
363
330
410
300
350
200
450
100
343
342
340
341
344
364
362

FIG.3

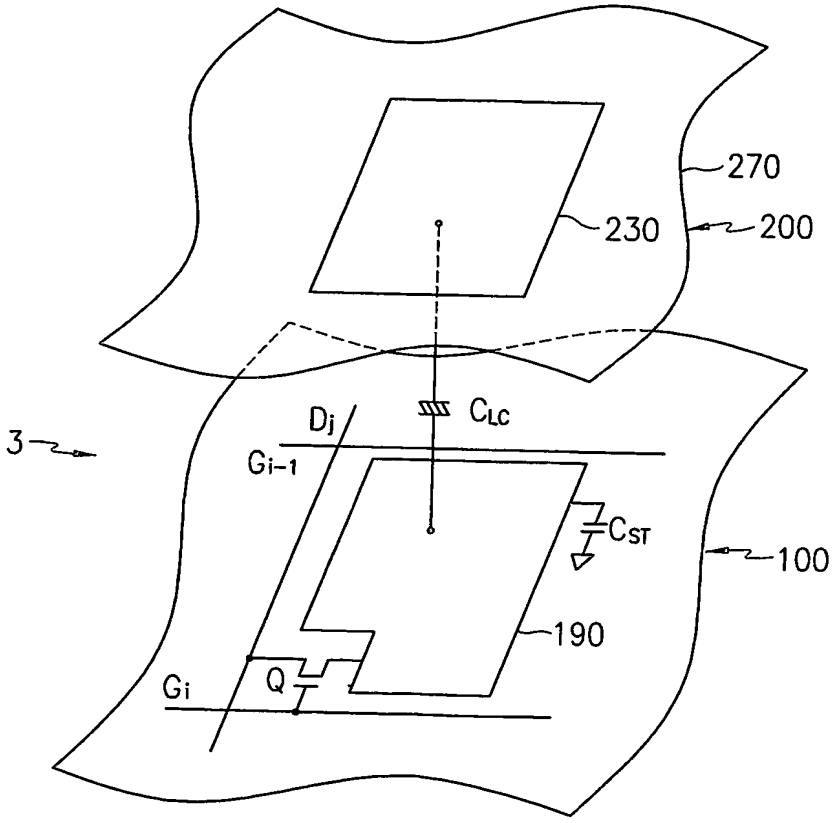


FIG.4

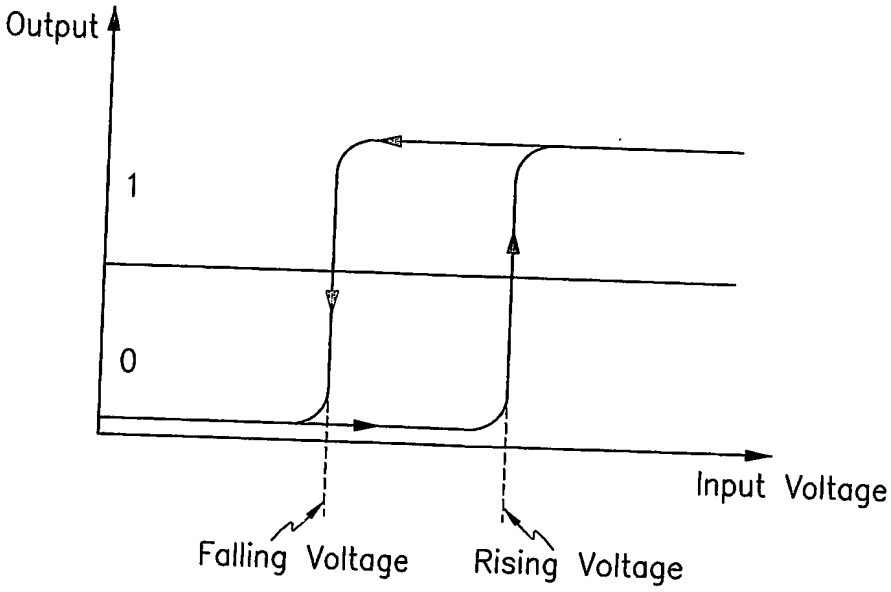
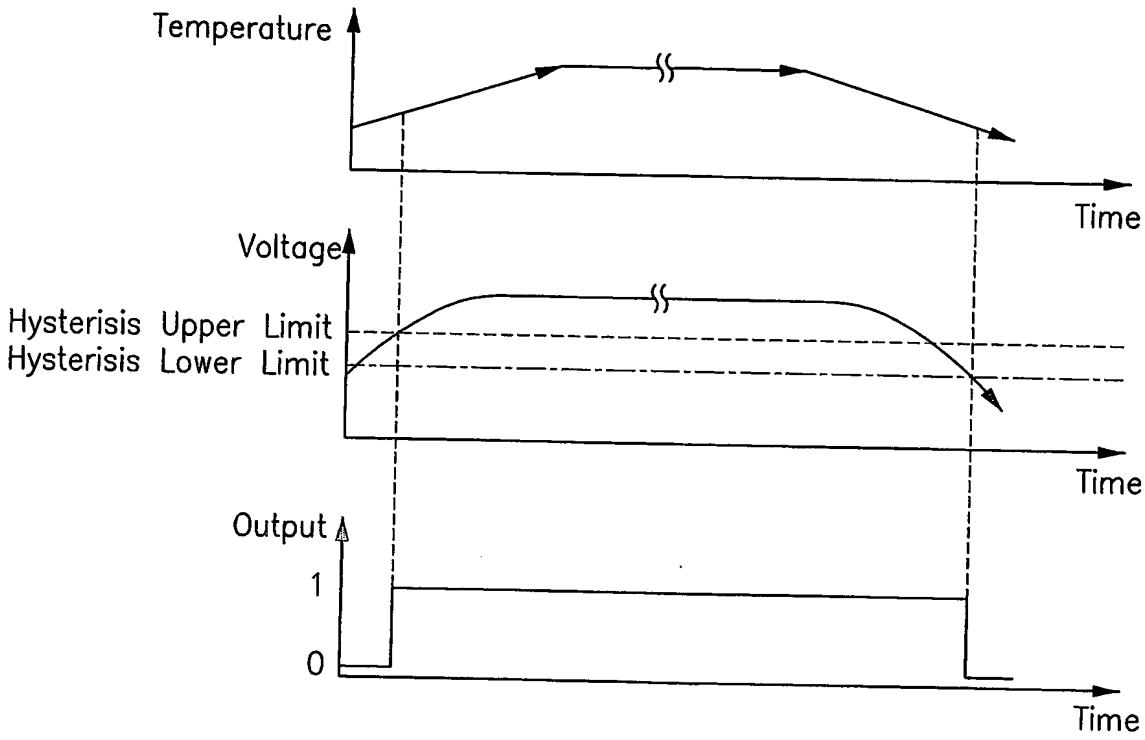


FIG.5


Temperature
Time
Voltage
Hysterisis Upper Limit
Hysterisis Lower Limit
Time
Output
1
0
Time